

MARKED-UP VERSION OF
ENGLISH TRANSLATION OF
INTERNATIONAL APPLICATION
AS ORIGINALLY FILED

~~DESCRIPTION~~

Attorney Docket No. 40404.54/ko

VOLTAGE DETECTING CIRCUIT AND BATTERY DEVICE USING SAME

~~TECHNICAL FIELD~~

[0001] -----BACKGROUND OF THE INVENTION

1. Field of the Invention

[0001] The present invention relates to a voltage detecting circuit for detecting the voltage of a battery or ~~the like other~~ similar device and outputting a signal that indicates whether ~~this~~ the voltage is high or low with respect to a predetermined voltage, and to a battery device suitable for battery-mounted using equipment that is able to fully utilize the potential of a the battery.

~~BACKGROUND ART~~ 2. Description of the Related Art

[0002] [0002] Patent Document 1 Japanese Patent Application Laid-Open No. H11-258280 discloses one example of a known conventional voltage detecting circuit and battery device using this circuit. Fig. 4 of this example shows a voltage detecting circuit 102 and a battery device 101 using this circuit. A battery device 101 includes a voltage detecting circuit 102;

series-connected resistors 104, 105, having one end grounded, and for generating an input voltage V_{BAT} of the voltage detecting circuit 102; and a battery 103 connected to the other end of the series-connected resistors 104, 105 that serves as the power source of a battery-mounted using equipment. The voltage of the battery 103 is divided by the series-connected resistors 104, 105, and this divided voltage is input into the voltage detecting circuit 102 as an input voltage V_{BAT} . The voltage detecting circuit 102 compares the input voltage V_{BAT} with a detection threshold value voltage V_{TH} . When the input voltage V_{BAT} is higher than the detection threshold value voltage V_{TH} , it judges, the voltage detecting circuit 102 determines that there to be is a battery residual quantity available and outputs a high-level voltage as a detected result from to an output terminal OUT. Conversely, when the input voltage V_{BAT} is lower than the detection threshold value voltage V_{TH} , it judges the voltage detecting circuit 102 determines that there to be is no battery residual quantity available and outputs a low-level voltage as a detected result from to the output terminal OUT. The output terminal OUT is connected to other electronic components (not shown in the drawing figure) which are included in the battery-mounted using equipment.

[0003] [0003] The voltage detecting circuit 102 includes a threshold value voltage generator 112 that selectively generates

separate different threshold value voltages (high-side and low-side) depending on whether the input voltage V_{BAT} ~~increases~~
(changes from low voltage to high voltage) or whether ~~it the~~
input voltage V_{BAT} drops (changes from high voltage to low voltage), a comparator 111 that compares this threshold value voltage (detection threshold value voltage V_{TH}) with the input voltage V_{BAT} , an inverter 124 that inverts the output of the comparator 111, an output switching element 113 that receives the output of the inverter 124 and outputs the voltage detection result from the output terminal OUT, and a reference voltage generator (REF) 114 that generates a reference voltage V_{REF} . The output of the inverter 124 is also used for selecting one of the two threshold value voltages in the threshold value voltage generator 112. The reference voltage V_{REF} is used as a reference for these threshold value voltages.

[0004] ~~[0004]~~ The threshold value voltage generator 112 and comparator 111 afford the achieve hysteresis of the input voltage detection. By virtue of this achievement, when the input voltage V_{BAT} approximates the detection threshold value voltage V_{TH} , the repeated turning ON and OFF of the output switching element 113 caused by minute levels of noise resulting in instability of the output of the output terminal OUT is prevented.

[0005] Patent Document 1: Japanese Patent Application Laid-Open No. H11-258280

DISCLOSURE OF THE INVENTION

PROBLEM TO BE SOLVED BY THE INVENTION

[0005] [0006] The battery device 101 selects the high-side threshold value voltage when the input voltage V_{BAT} ~~rises~~increases and selects the low-side threshold value voltage when ~~it~~the input voltage V_{BAT} drops. Accordingly, when the battery 103 is removed, as the input voltage V_{BAT} drops, the low-side threshold value voltage is set as the detection threshold value voltage V_{TH} . When the battery 103 is then ~~refitted~~reinserted, as the input voltage V_{BAT} ~~rises~~increases, the high-side threshold value voltage is set.

[0006] [0007] Fig. 5 shows the relationship between the input voltage V_{BAT} and the detection threshold value voltage V_{TH} . The ~~drawing~~figure shows a case in which there is only a little residual battery quantity available, that is to say, a case in which the battery voltage has gradually dropped from a state in which the input voltage V_{BAT} exceeds a high-side threshold value voltage (for example, 2.9V) in the initial usage period of the battery 103 to a state in which the input voltage V_{BAT} is a voltage (for example 2.6V) between the high-side threshold value voltage and a low-side threshold value voltage (for example

2.5V). When the battery 103 is removed and the input voltage V_{BAT} drops and intersects the low-side threshold value voltage, the output terminal OUT switches from the high level to the low level and the detection threshold value voltage V_{TH} is set to the high-side threshold value voltage. When the battery 103 is then ~~refitted~~reinserted subsequent to the input voltage V_{BAT} having reached the ground potential (0V), the input voltage V_{BAT} ~~rises~~increases and exceeds the low-side threshold value voltage value. However, it does not reach and does not intersect with the high-side threshold value voltage and, as a result, the output terminal OUT remains at the low level. Accordingly, while the battery~~-mounted~~using equipment operates as if residual battery quantity is available until the battery 103 is removed, when the battery 103 is temporarily removed and then ~~refitted~~reinserted, a phenomenon is established in which there is a state of no residual quantity of the battery 103 available ~~established~~, which prevents the battery~~-mounted~~using equipment from being started up ~~is produced~~starting up. This precludes the battery~~-mounted~~using equipment from being able to be used to the limit of the available residual quantity of the battery 103 and reduces the usable ~~timelife~~ of the battery.

{0008} With the foregoing in view, it is an object

SUMMARY OF THE INVENTION

[0007] In order to overcome the problems described above, preferred embodiments of the present invention ~~to~~ provide a voltage detecting circuit in which a battery assembled in ~~battery-mounted~~ using equipment can be used with certainty until up to its usage limit, and to a battery device using this circuit.

MEANS FOR SOLVING THE PROBLEM

[0008] ~~[0009]~~ In order to resolve the problem described above, the A voltage detecting circuit according to a preferred embodiment of the present invention ~~comprises~~ includes an input voltage comparing circuit that controls the opening and closing of an output switching element in response to an output of the comparison of a first threshold value voltage or a second threshold value voltage lower than the first threshold value voltage with an input voltage, and selects the second threshold value voltage when the input voltage changes from a low voltage to a high voltage and intersects the first threshold value voltage, and selects the first threshold value voltage when the input voltage changes from the high voltage to the low voltage and intersects the second threshold value voltage; and a threshold value voltage ~~foreibly~~ setting circuit that compares a third threshold value voltage lower than the second threshold value voltage with the input voltage and, when the input voltage

changes from a low voltage to a high voltage and intersects the third threshold value voltage, outputs a pulse for a predetermined period thereafter so that the second threshold value is ~~forcibly~~—selected in the input voltage comparing circuit, the second threshold value voltage being compared with the input voltage in the input voltage comparing circuit when the input voltage ~~rises up~~increases.

[0009] [0010] It is preferable that the threshold value voltage ~~forcibly~~—setting circuit ~~selects~~selects a fourth threshold value voltage lower than the third threshold value voltage when the input voltage changes from a low voltage to a high voltage and intersects the third threshold value voltage, and that ~~it—selects~~the threshold value voltage setting circuit selects the third threshold value voltage when the input voltage changes from a high voltage to a low voltage and intersects the fourth threshold value voltage.

[0010] [0011] The battery device according to another preferred embodiment of the present invention ~~comprises~~includes the above-described voltage detecting circuit, series-connected resistors, having one end grounded, and for generating the input voltage of the voltage detecting circuit, and a battery that is connected to the other end of the series-connected resistors.

EFFECTS OF THE INVENTION

[0011] ~~[0012]~~ According to the preferred ~~embodiment~~embodiments of the present invention, because the voltage detecting circuit and battery device using this circuit use a third threshold value voltage lower than a first threshold value voltage and second threshold value voltage and, when the input voltage ~~rises up~~increases and reaches the third threshold value voltage, a threshold value voltage ~~foreibly~~foreibly setting circuit sets in the input voltage comparing circuit the second threshold value voltage, a battery assembled in battery-mounted using equipment can be used ~~with certainty until up to~~until up to its usage limit.

[0012] Other features, elements, processes, steps, characteristics and advantages of the present invention will become more apparent from the following detailed description of preferred embodiments of the present invention with reference to the attached drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] ~~[0013]~~ [Fig. 1] Fig.1 is a circuit diagram of the voltage detecting circuit according to ~~an~~a preferred embodiment of the present invention and battery device using ~~this~~the circuit.

[0014] [Fig. 2] Fig. 2 is a circuit diagram of a threshold value voltage generator of Fig. 1.

[0015] [Fig. 3] Fig. 3 is an operating waveform diagram of Fig. 1.

[0016] [Fig. 4] Fig. 4 is a circuit diagram of a conventional voltage detecting circuit and battery device using this the circuit.

[0017] [Fig. 5] Fig. 5 is an operating waveform diagram of Fig. 4.

EXPLANATION OF REFERENCE NUMERALS DETAILED DESCRIPTION OF

PREFERRED EMBODIMENTS

[0014] 1. Battery device

2. Voltage detecting circuit

3. Battery

4, 5. Resistors

7. Input voltage comparing circuit

8. Threshold value voltage forcibly setting circuit

13. Switching element

V_{in} . Input voltage

V_{th} . Detection threshold value voltage

V_{th1} . First threshold value voltage

V_{th2} . Second threshold value voltage

V_{th3} . Third threshold value voltage

V_{TH} , fourth threshold value voltage

BEST MODE FOR CARRYING OUT THE INVENTION

[0018] [0015] The best mode for carrying out the preferred embodiments of the present invention will be hereinafter described with reference to the drawings. Fig. 1 is a circuit diagram of the voltage detecting circuit 2 according to an ~~a~~ preferred embodiment of the present invention and a battery device 1 using ~~this~~ the circuit.

[0019] [0016] The battery device 1 includes a voltage detecting circuit 2; series-connected resistors 4, 5, having one end grounded, for generating an input voltage V_{BAT} of the voltage detecting circuit 2; and a battery 3 connected to the other end of the series-connected resistors 4, 5 which serves as the power source of ~~the battery~~ ~~mounted~~ ~~using~~ equipment. The voltage of the battery 3 is divided by the series-connected resistors 4, 5, and this divided voltage is input as the input voltage V_{BAT} to the voltage detecting circuit 2. The voltage detecting circuit 2 compares the input voltage V_{BAT} with a detection threshold value voltage V_{TH} . When the input voltage V_{BAT} is higher than the detection threshold value voltage V_{TH} ~~it judges~~, the voltage detecting circuit 2 determines that there ~~to be~~ ~~is~~ a battery residual quantity available and outputs a high-level voltage as a detected result ~~from~~ ~~to~~ an output

terminal OUT. Conversely, when the input voltage V_{BAT} is lower than the detection threshold value voltage V_{TH} , ~~it judges the voltage detecting circuit 2 determines that there to be is~~ no battery residual quantity available and outputs a low-level voltage as a detected result ~~from to~~ the output terminal OUT. The output terminal OUT is connected to other electronic components (not shown in the ~~drawing~~figure) which are included in the ~~battery-mounted using~~ equipment. The resistance value ratio, that is to say, the voltage-dividing ratio of the series-connected resistors 4, 5 is determined by the specification of the electronic components which are included in the ~~battery-mounted using~~ equipment. Resistance values are sufficiently large enough to minimize the direct current value.

[0020] ~~{0017}~~ The voltage detecting circuit 2 mainly includes an input voltage comparing circuit 7 and threshold value voltage V_{TH} ~~foreibly~~ setting circuit 8. The input voltage comparing circuit 7, using as a detection threshold value voltage V_{TH} a first threshold value voltage V_{th1} or second threshold value voltage V_{th2} lower than the first threshold value voltage V_{th1} , controls the opening and closing (ON and OFF) of an output switching element 13 in response to the output of the comparison of the detection threshold value voltage V_{TH} with the input voltage V_{BAT} . As well as this, when the input voltage V_{BAT} ~~rises~~increases, that is to say, changes from a low voltage to a

high voltage and intersects the first threshold value voltage V_{th1} , the input voltage comparing circuit 7 selects the second threshold value voltage V_{th2} as the detection threshold value voltage V_{TH} , and when the input voltage V_{BAT} drops, that is to say, changes from a high voltage to a low voltage and intersects the second threshold value voltage V_{th2} , it selects the first threshold value voltage V_{th1} as the detection threshold value voltage V_{TH} . Accordingly, the input voltage comparing circuit 7 ~~imparts achieves~~ achieves hysteresis to the detection input voltage whereupon, when the input voltage approximates the detection threshold value voltage V_{TH} , the repeated turning ON and OFF of the output switching element 13 is prevented. Then, the instability of output of the output terminal OUT resulting in generation of short-circuit current or noise in the voltage detecting circuit 2 ~~itself~~ and the electronic components which are included in the battery-mounted ~~using~~ equipment is prevented. The output switching element 13 is an N-type MOS transistor of an open-drain configuration that is pulled up to any power source by a resistor (not shown in the drawing) ~~in the outer figure~~ outside of the voltage detecting circuit 2. The output switching element 13 is not limited to an N-type MOS transistor and can be any other suitable switch.

[0021] ~~[0018]~~ The threshold value voltage ~~forcibly~~ setting circuit 8 compares a third threshold value voltage V_{th3} lower than

the second threshold value voltage V_{th2} with the input voltage V_{BAT} and, when the input voltage V_{BAT} changes from a low voltage to a high voltage and intersects the third threshold value voltage V_{th3} , outputs a pulse for a predetermined period ~~thereafter~~, to the input voltage comparing circuit 7 so that the second threshold value voltage V_{th2} is forcibly selected in the input voltage comparing circuit 7. By virtue of this, when the input voltage V_{BAT} ~~rises up~~ increases from the ground potential, the second threshold value voltage V_{th2} is compared with the input voltage V_{BAT} in the input voltage comparing circuit 7.

[0022] ~~[0019]~~ Furthermore, when the input voltage V_{BAT} changes from a low voltage to a high voltage and intersects the third threshold value voltage V_{th3} , the threshold value voltage ~~forcibly~~ setting circuit 8 selects a fourth threshold value voltage V_{th4} lower than the third threshold value voltage V_{th3} , and when the input voltage V_{BAT} changes from a high voltage to a low voltage and intersects the fourth threshold value voltage V_{th4} , it selects the third threshold value voltage V_{th3} . Accordingly, hysteresis is also ~~imparted~~ achieved in the threshold value voltage ~~forcibly~~ setting circuit 8 and, when the input voltage V_{BAT} approximates the third threshold value voltage V_{th3} , instability of the output of the threshold value voltage ~~forcibly~~ setting circuit 8 is prevented.

[0023] [0020] More specifically, the voltage detecting circuit 2 includes an inverter 24 that inverts the output of the input voltage comparing circuit 7, an output switching element 13 that is turned ON and OFF in accordance with the output of the inverter 24, ~~an~~a NOR circuit 23 that controls the input voltage comparing circuit 7 ~~itself~~ based on the ~~input of the~~ output of the input voltage comparing circuit 7 and the output of the threshold value voltage ~~possibly~~ setting circuit 8, and a reference voltage generator (REF) 14 into which the voltage of the battery 3 is directly input and that generates and outputs a reference voltage V_{REF} .

[0024] [0021] The input voltage comparing circuit 7 includes a threshold value voltage generator 12 that selectively generates a first or a second threshold value voltage as the detection threshold value voltage V_{TH} , and a comparator 11 that compares the detection threshold value voltage V_{TH} input into an inversion input terminal with the input voltage V_{BAT} input into a non-inversion input terminal.

[0025] [0022] The threshold value voltage generator 12, as shown in Fig. 2, includes resistors 31, 32 connected in series between the reference voltage V_{REF} and ground potential, and a switch 33 provided in parallel with the resistor 31 that is controlled by the input voltage of a terminal CNTLa. The connection point between the resistor 31 and the resistor 32

~~constitutes~~defines an output terminal OUTa that ~~has~~connection ~~is connected~~ with the inversion input terminal of the comparator 11. The terminal CNTLa ~~has~~connection ~~is connected~~ with the output of the NOR circuit 23 and is controlled by the output of the input voltage comparing circuit 7 ~~itself~~ and the threshold value voltage ~~forcibly~~ setting circuit 8 as described above. If the voltage of the terminal CNTLa is a high level, the switch 33 closes and the reference voltage V_{REF} is output from the output terminal OUTa as the first threshold value voltage V_{th1} . If the voltage of the terminal CNTLa is a low level, the switch 33 opens and the voltage resulting from the voltage division of the reference voltage V_{REF} by the resistor 31 and resistor 32 is output from the output terminal OUTa as the second threshold value voltage V_{th2} .

[0026] ~~[0023]~~ Whilst While hysteresis of the input voltage V_{BAT} is ~~afforded~~achieved in the input voltage comparing circuit 7 by virtue of the threshold value voltage generator 12 and the comparator 11, hysteresis may ~~obviously be afforded~~be achieved by other circuit configurations.

[0027] ~~[0024]~~ The threshold value voltage ~~forcibly~~ setting circuit 8 includes a second threshold value voltage value generator 16 that selectively generates third and fourth threshold value voltages, a second comparator 15 that compares the third or fourth threshold value voltages input into the non-

inversion input terminal with the input voltage V_{BAT} input into the inversion input terminal, ~~an N-type MOS transistor a switching element~~ ¹⁷ into which the output of the second comparator 15 is input, a constant current source 18 that ~~has connection is connected at one end~~ to the output of the ~~N-type MOS transistor~~ ^{switching element} 17 and at the other end ~~has connection is connected~~ to the reference voltage V_{REF} , a capacitor 19 that ~~has connection is connected at one end~~ to the output of the ~~N-type MOS transistor~~ ^{switching element} 17 and at the other end ~~has connection is connected~~ to the ground potential, an inverter circuit 20 that inverts the output of the ~~N-type MOS transistor~~ ^{switching element} 17, an inverter circuit 21 that inverts the output of the second comparator 15, and an AND circuit 22 based on the inputting of the outputs of the inverter circuits 20, 21 that outputs a signal TP to the ~~an~~ input terminal of the NOR circuit 23 based on the outputs of the inverter circuits 20, 21, as described above. Switching element 17 can be an N-type MOS transistor or any other suitable switch.

[0028] [0025] The N-type MOS transistor ~~The switching element~~ 17, constant current source 18, capacitor 19, and inverter 20 ~~constituted~~ ^{define} a circuit that generates a delay (for example, 1 millisecond). This circuit may be replaced by a counter that generates a delay based on the input of a clock that serves as a reference. A pulse ~~of having a pulse width~~

established by this delay is generated by the ~~circuit that generates this delay, the inverter 21 and, the AND circuit 22, and the circuit that generates the delay.~~

[0029] [0026] As shown in Fig. 2, the second threshold value voltage generator 16 includes resistors 34, 35, 36 connected in series between the reference voltage V_{REF} and ground potential, and a switch 37 provided in parallel with the resistor 34 that is controlled by the input voltage of a terminal CNTLb. The connection point of the resistor 35 and resistor 36 serves as an output terminal OUTb and connects to the non-inversion input terminal of the second comparator 15. The terminal CNTLb is connected to the output of the second comparator 15. If the voltage of the terminal CNTLb is a high level, the switch 37 closes and the voltage obtained by the voltage-division of the reference voltage V_{REF} by the resistor 35 and resistor 36 is output from the output terminal OUTb as the third threshold value voltage V_{th3} , ~~while if~~. If the voltage of the terminal CNTLb is a low level, the switch 37 opens and the voltage obtained by the voltage-division of the reference voltage V_{REF} by the resistor 34 and resistor 35 connected in series and the resistor 36 is output from the output terminal OUTb as the fourth threshold value voltage V_{th4} .

[0030] [0027] As ~~is~~ described above, ~~the~~ imparting of achieving hysteresis by the threshold value voltage ~~forcibly~~

setting circuit 8 is preferable from the viewpoint of preventing instability of the output of the threshold value voltage ~~forcibly~~-setting circuit 8. However, because this output is not output to the exterior of the voltage detecting circuit 2, the extent of short-circuit current or noise generated as a result is not large. Accordingly, the omission thereof may be considered. In this case, the second threshold value voltage generator 16, in the absence of the resistor 34 and the terminal CNTLb shown in Fig. 2, outputs only the third threshold value voltage V_{th3} .

[0031] ~~[0028]~~ The operation of the voltage detecting circuit 2 will be described hereinafter with reference to Fig. 3 while specifically indicating the examples of the voltage values. However, other voltage values may be used. When the input voltage V_{BAT} (for example, 2.6V) is higher than the second threshold value voltage V_{th2} (for example 2.5V) serving as the detection threshold value voltage V_{TH} , the output of the output terminal OUT is a high level and a residual quantity of the battery 3 is ~~judged~~-determined to be available. When the battery 3 is removed in this state, the input voltage V_{BAT} drops. When the input voltage V_{BAT} changes from a high voltage to a low voltage and intersects the second threshold value voltage V_{th2} (for example 2.5V), the output voltage of the comparator 11 changes from a high level to a low level. Subsequently, the

output switching element 13 is turned ON to establish the output terminal OUT at the low level and, in addition, the detection threshold value voltage V_{TH} generated by the threshold value voltage generator 12 changes to the first threshold value voltage V_{th1} (for example, 2.9V). Although not shown in the drawingsfigure, when the input voltage V_{BAT} drops and the input voltage V_{BAT} changes from a high voltage to a low voltage and intersects the fourth threshold value voltage V_{th4} (for example, 2.0V), the output voltage of the second comparator 15 changes from a low level to a high level and the threshold value voltage generated by second threshold value voltage generator 16 becomes the third threshold value voltage V_{th3} (for example, 2.4V).

[0032] ~~[0029]~~ The operatingoperation of the voltage detecting circuit 2 when the input voltage V_{BAT} ~~rises up~~increases from the ground potential as a result of ~~the refitting~~orreinserting the battery 3 will be hereinafter described. When the input voltage V_{BAT} changes from a low voltage to a high voltage and intersects the third threshold value voltage V_{th3} (for example 2.4V) generated by the second threshold value voltage generator 16, the output voltage of the second comparator 15 changes from a high level to a low level. Subsequently, the N-type MOStransistorswitching element 17 is turned OFF and, while the voltage of the capacitor 19 begins to gradually increase from the ground potential due to the current

from the constant current source 18, the inverter 20 ~~judges~~determines the input as a low level during the delay time (for example, 1 millisecond) determined from the values of the current from the constant current source 18 and the capacitance of the capacitor 19. Accordingly, because the inputs of the AND circuit 22 are both high level, the output TP ~~thereof~~of the AND circuit 22 is also a high level, while the output of the NOR circuit 23, irrespective of the other input, that is to say, the control signal from the input voltage comparing circuit 7 ~~itself~~, is a low level. As a result, the detection threshold value voltage V_{TH} generated by the threshold value voltage generator 12 is the second threshold value voltage V_{th2} (for example, 2.5V). Accompanying these operations, the threshold value voltage generated by the second threshold value voltage generator 16 changes to the fourth threshold value voltage V_{th4} (for example, 2.0V).

[0033] [0030] When the input voltage V_{BAT} further ~~rises~~increases and the input voltage V_{BAT} changes from a low voltage to a high voltage and intersects the second threshold value voltage V_{th2} (for example 2.5V) during the period of the above delay time (for example, 1 millisecond), the output voltage of the comparator 11 changes from a low level to a high level. Subsequently, the output switching element 13 is turned OFF to establish the output terminal OUT at the high level. In

addition, because the input to the NOR circuit 23 is a high level, even after the above delay time (for example, 1 millisecond) has elapsed, the detection threshold value voltage V_{TH} generated by the threshold value voltage generator 12 remains as the second threshold value voltage V_{th2} (for example, 2.5V). Accordingly, even if the input voltage V_{BAT} is a lower voltage than the first threshold value voltage V_{th1} (for example 2.9V), provided that the second threshold value voltage V_{th2} (for example 2.5V) is exceeded, a battery residual quantity is judgeddetermined as being available.

[0034] [0031] On the other hand, if If the input voltage V_{BAT} does not intersect the second threshold value voltage V_{th2} (for example, 2.5V) during the period of the above delay time (for example, 1 millisecond) even if changing from a low voltage to a high voltage, the output TP of the AND circuit 22 is established at the low level and the output of the NOR circuit 23 is established at the high level, while the detection threshold value voltage V_{TH} generated by the threshold value voltage generator 12 returns to the first threshold value voltage V_{th1} (for example, 2.9V). The output voltage of the comparator 11 remains at a low level and, with the output terminal OUT unchanged at the low level, there is judgeddetermined to be no residual battery quantity available. The right sectionportion of Fig. 3 shows a state where the input

voltage V_{BAT} ~~constitutes~~defines a voltage (for example, 2.45V) that has not reached the second threshold value voltage V_{th2} (for example 2.5V) and there is ~~judged~~determined to be no residual battery quantity available.

[0035] ~~[0032]~~—Although not shown in the ~~drawings~~figures, it is notable that the ~~judgment~~determination of no available battery residual quantity also includes those instances in which, in the end, even if the input voltage V_{BAT} does reach the second threshold value voltage V_{th2} (for example, 2.5V), the rise from the third threshold value voltage V_{th3} (for example, 2.4V) to the second threshold value voltage V_{th2} (for example, 2.5V) takes longer than the above delay time (for example, 1 millisecond). Accordingly, for this reason, to prevent the ~~judgment~~determination that there is no residual quantity available despite the battery residual quantity not having reached its usage limit, the above delay time must be determined in advance with consideration of the parasitic capacitance of the battery 3 and resistors 4 and 5 to be greater than the actual time taken from the third threshold value voltage V_{th3} (for example, 2.4V) to the second threshold value voltage V_{th2} (for example, 2.5V) when an appropriate battery residual quantity is available.

[0036] ~~[0033]~~—As ~~is~~described above, the voltage detecting circuit 2 according to ~~an~~the present preferred embodiment of the

present invention, and the battery device 1 using this circuit, is provided with a third threshold value voltage V_{th3} lower than first and second threshold value voltages V_{th1} , V_{th2} and, ~~because~~, when the input voltage V_{BAT} ~~increases up~~increases and reaches the third threshold value voltage V_{th3} , the threshold value voltage ~~forcibly~~ setting circuit 8 sets in the input voltage comparing circuit 7 the second threshold value voltage V_{th2} . Accordingly, even when the battery of only a little residual quantity available is temporarily removed and then ~~refitted~~reinserted, a phenomenon is established in which there is a state of no residual quantity of the battery available and in which the battery-mounted using equipment is doesn't start up can be prevented from being started up can be prevented. In this way, when the voltage detecting circuit 2 and the battery device 1 using this circuit are assembled in battery-mounted using equipment, the battery is able to be used with certainty until the usage limit ~~thereof~~ and the useable timelife of the battery can be substantially extended.

[0037] ~~[0034]~~ The present invention is not restricted to the ~~embodiment~~preferred embodiments described above and, within the scope of the items described in the claims, a range of modifications may be made thereto.

[0038] While the present invention has been described with respect to preferred embodiments thereof, it will be apparent to

those skilled in the art that the disclosed invention may be modified in numerous ways and may assume many embodiments other than those specifically set out and described above.